

HiPEAC Sibiu Workshop Agenda

European Network of Excellence on High Performance and Embedded Architecture and Compilation (HiPEAC) Workshop

<http://www.hipeac.net/>

„Lucian Blaga” University of Sibiu (LBUS), 2-nd April 2013

<http://www.ulbsibiu.ro/ro/>

Location: Senate Hall, Rectorate Building, Bulevardul Victoriei, Nr. 10, Sibiu, 550024, România

(see <https://maps.google.ro/maps?hl=ro&q=bulevardul+victoriei+nr+10+sibiu&ie=UTF-8&hq=&hnear=0x474c67882d7c3d43:0xceed9e1ab8079c0d,Bulevardul+Victoriei,+Sibiu&gl=ro&ei=Uc88UfGMH8GJtAaPuoHADw&ved=0CDIQ8gEwAA>)

09:00 - 09:40 *HiPEAC Steering Committee Staff Meeting with the LBUS Rector (Professor Ioan Bondrea - Rector of LBUS, Prof. Koen de Bosschere, Prof. Rainer Leupers, Prof. Kostas Magoutis, Prof. Lucian Vintan)*

09:50 - 10:10 *Welcome Words, Prof. Lucian Vintan, Computer & Electrical Engineering Department, LBUS, The Academy of Technical Sciences of Romania; Prof. Ioan Bondrea - Rector of LBUS; Prof. Liviu Rosca – Dean of the Engineering School; Prof. Daniel Volovici, Head of Computer & Electrical Engineering Department, LBUS*

10:10 - 10:45 *HiPEAC network and activities overview, Prof. Koen de Bosschere (<https://syslab.elis.ugent.be/kdbosche>), HiPEAC Coordinator, Computing Systems Lab, Ghent University, Belgium*

10:45 - 11:20 *System performance modeling and binary translation research at Ghent University, Prof. Koen de Bosschere, HiPEAC Coordinator, Computing Systems Lab, Ghent University, Belgium*

11:20 - 11:35 *Coffee Break*

11:35 – 12:10 *Scalable architectures and networked storage research at ICS-FORTH, Prof. Kostas Magoutis (<http://www.ics.forth.gr/~magoutis/>), Computer Science Foundation for Research and Technology-Hellas, Greece*

12:10 – 12:45 *System-Level Design Technologies for Embedded Multicore Devices, Prof. Rainer Leupers (<http://www.ice.rwth-aachen.de/institute/members/personen/rainer-leupers/>), Institute for Communication Technologies and Embedded Systems, Aachen University, Germany*

12:45-14:15 *Lunch (LBUS Restaurant)*

14:15 – 15:10 *Multi-Objective Optimization of Advanced Micro-Architectures using Domain-Knowledge (Computer Architecture on-going Research Projects at LBUS), Prof. Lucian Vintan*

(<http://webspaces.ulbsibiu.ro/lucian.vintan/html/>), Computer & Electrical Engineering Department, LBUS, The Academy of Technical Sciences of Romania

15:10 – 15:45 *Parallel and Distributed Systems Research at Politehnica University of Bucharest,*
Prof. Nicolae Tapus

(<https://csite.cs.pub.ro/index.php/en/component/comprofiler/?task=userProfile&user=70>),
Computer Science & Engineering Department, „Politehnica” University of Bucharest, The Academy of Technical Sciences of Romania

15:45 - 16:00 *Break*

16:00-16:35 *Computer Engineering Research at Technical University University of Cluj-Napoca,*
Prof. Sergiu Nedevschi (<http://users.utcluj.ro/~nedevski/>), Computer Science & Engineering Department, Technical University of Cluj-Napoca, The Academy of Technical Sciences of Romania

16:35 - 17:10 *Research vision and projects of Research Center in Computer and Information Technology from UPT,* **Prof. Marius Marcu** (<http://bigfoot.cs.upt.ro/~mmarcu/>) and **Dr. Alexandru Amaricai** (<http://bigfoot.cs.upt.ro/~amaricai/>), Computer Science & Engineering Department, „Politehnica” University of Timisoara, Romania

17:10 - 17:45 *Continental Automotive Systems Sibiu - General Presentation; Continental Sibiu Embedded Capabilities,* **Liviu Balan** – Research General Manager and **Sebastian Olteanu** - Head of R&D Sibiu BUTransmission, Continental Automotive Systems R&D Sibiu, Romania

17:45 - 18:00 *Closing Remarks,* **Prof. Koen de Bosschere**, HiPEAC Coordinator, **Prof. Lucian Vintan**, LBUS, Romania

19:30 - 22:00 *Dinner, offered by HiPEAC*

ORGANIZING COMMITTEE

Prof. Lucian Vintan
Prof. Daniel Volovici
Assoc. Prof. Remus Brad
Assoc. Prof. Adrian Florea
Dr. Radu Cretulescu
Dr. Arpad Gellert
Dr. Daniel Morariu